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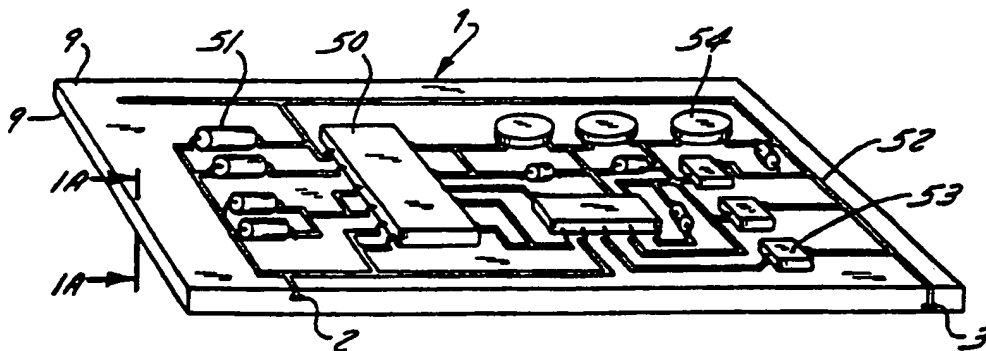
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INTERNATIONAL APPLICATION PUBLISHED UNDER THE PATENT COOPERATION TREATY (PCT)

<b>(51) International Patent Classification <sup>5</sup> :</b> <b>H05K 1/00</b>	<b>A1</b>	<b>(11) International Publication Number:</b> WO 93/14612 <b>(43) International Publication Date:</b> 22 July 1993 (22.07.93)
<b>(21) International Application Number:</b> PCT/US92/02164 <b>(22) International Filing Date:</b> 10 March 1992 (10.03.92)  <b>(30) Priority data:</b> 819,482 10 January 1992 (10.01.92) US  <b>(71) Applicant:</b> THE SCABBARD CORP. [US/US]; c/o Whitman & Ransom, 200 Park Avenue, New York, NY 10166 (US).  <b>(71)(72) Applicant and Inventor:</b> DUBRUCQ, Denyse [US/US]; 7127 W. Washington Street, West Allis, WI 53214 (US).  <b>(74) Agent:</b> BOYLE, James, F.; Nilles & Nilles, First Wisconsin Center, 777 East Wisconsin Avenue, Suite 2000, Milwaukee, WI 53202 (US).		<b>(81) Designated States:</b> JP, KR, European patent (AT, BE, CH, DE, DK, ES, FR, GB, GR, IT, LU, MC, NL, SE).  <b>Published</b> <i>With international search report.</i>

**(54) Title:** SHEET BATTERIES AS SUBSTRATE FOR ELECTRONIC CIRCUITS



**(57) Abstract**

The invention relates to a sheet battery (1) used as a substrate for an electronic circuit. The technical problem associated with prior battery powered electronic devices in that construction of the prior devices require separate components for the battery, a substitute material on which the electronic circuits are placed, connection of the circuit to the battery, and an encasement structure. The invention solves this technical problem by using sheet battery material (1) as a substrate for electronic circuits (52) for electronic devices (50, 51, 52, 53 and 54) to eliminate the weight and bulk of a battery as a separate circuit component and also to eliminate the weight and bulk of fiberglass substrate or flex substrate as a printed circuit material. Inserts (70 and 75) with wiring built in can give vertical wiring between two or more circuit layers and can support electronic components which have leads connecting wiring on more than one layer of a printed circuit. Connector clips (80) inserted when the connector pins are polarized target connection to power (2) and to ground (3) components of one or more layers of sheet battery.

## INTERNATIONAL SEARCH REPORT

 International application No.  
 PCT/US92/02164

<b>A. CLASSIFICATION OF SUBJECT MATTER</b> IPC(5) : H05K 1/00 US CL : 174/260 According to International Patent Classification (IPC) or to both national classification and IPC		
<b>B. FIELDS SEARCHED</b> Minimum documentation searched (classification system followed by classification symbols) U.S. : 174/260, 250, 255; 429/124, 191, 192; 29/832 Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched Electronic data base consulted during the international search (name of data base and, where practicable, search terms used)		
<b>C. DOCUMENTS CONSIDERED TO BE RELEVANT</b>		
Category*	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
A,E	US, A, 5,124,508 (DuBrucq) 23 June 1992 See entire document.	1-32
A	US, A, 5,073,684 (Miyabayashi) 17 December 1991 See entire document.	1-32
A	US, A, 4,789,610 (Kondo et al.) 06 December 1988 See entire document.	1-32
A	US, A, 4,030,001 (Medley, Jr. et al.) 14 June 1977 See entire document.	1-32
A	JP, A, 2-121,384 (Miyabayashi) 09 May 1990	1-32
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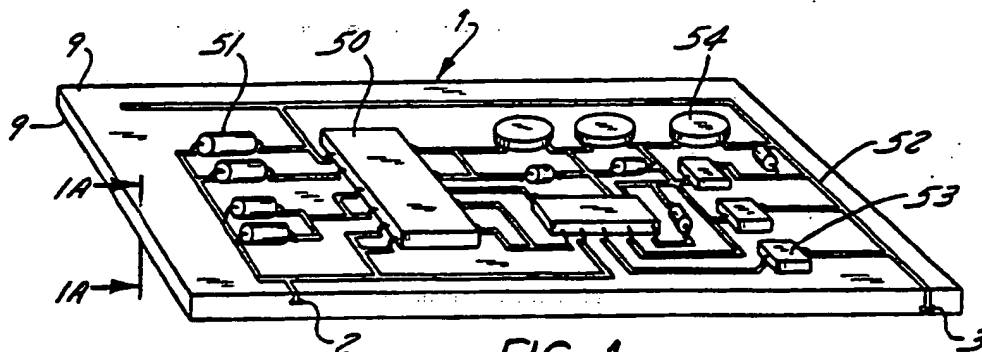


FIG. 1

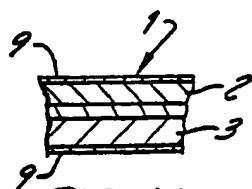


FIG. 1A

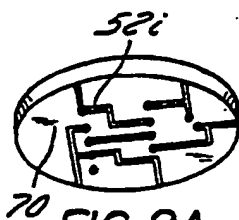


FIG. 2A

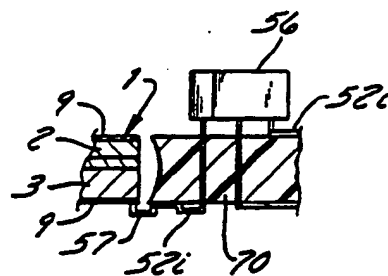


FIG. 2B

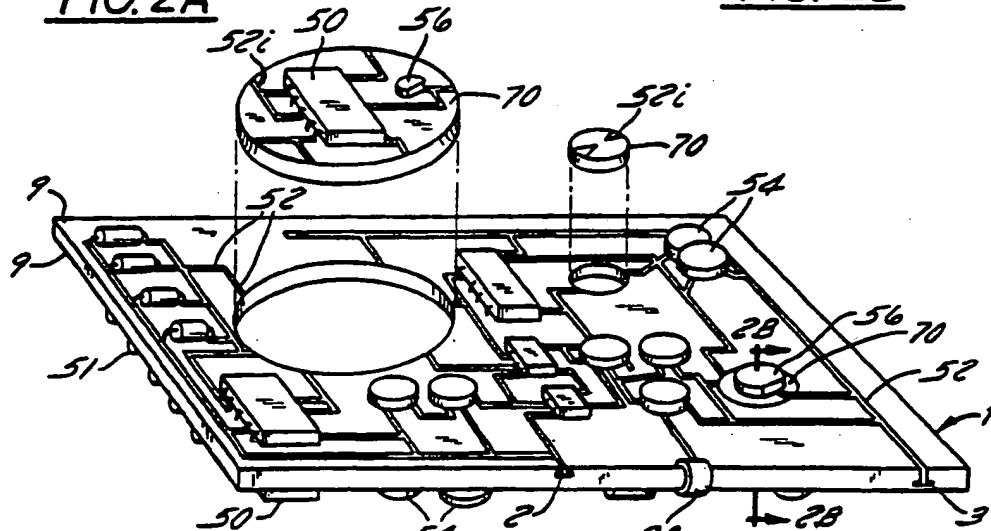


FIG. 2

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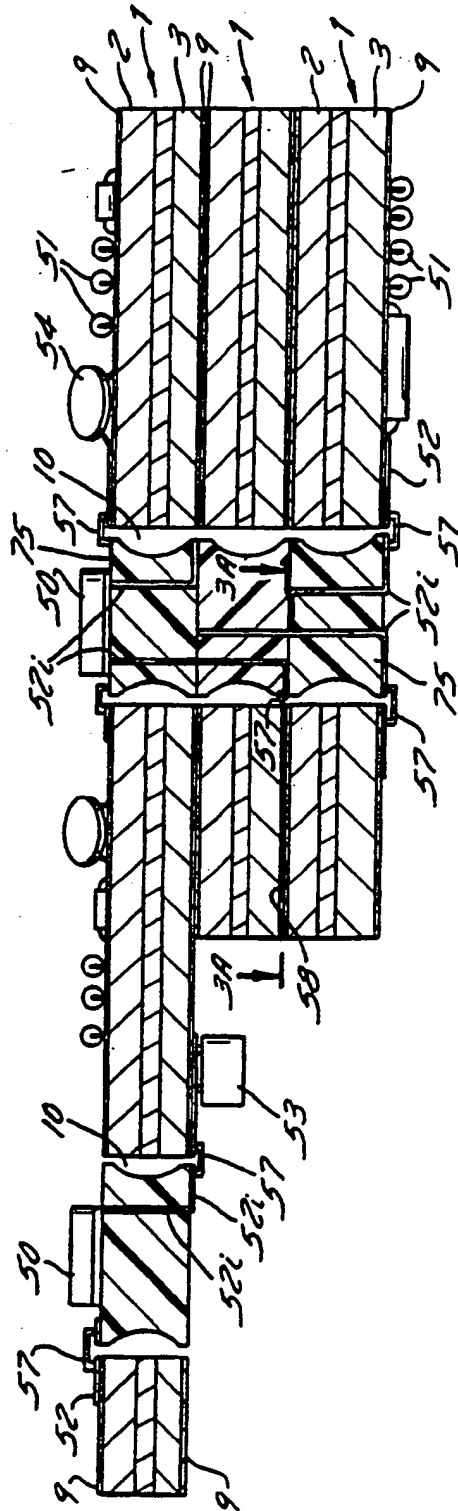


FIG. 3

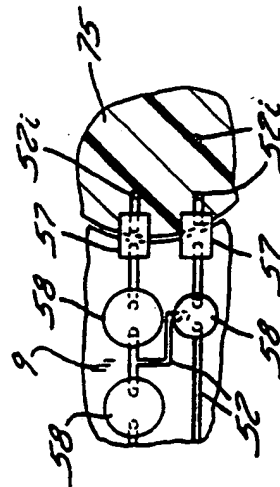
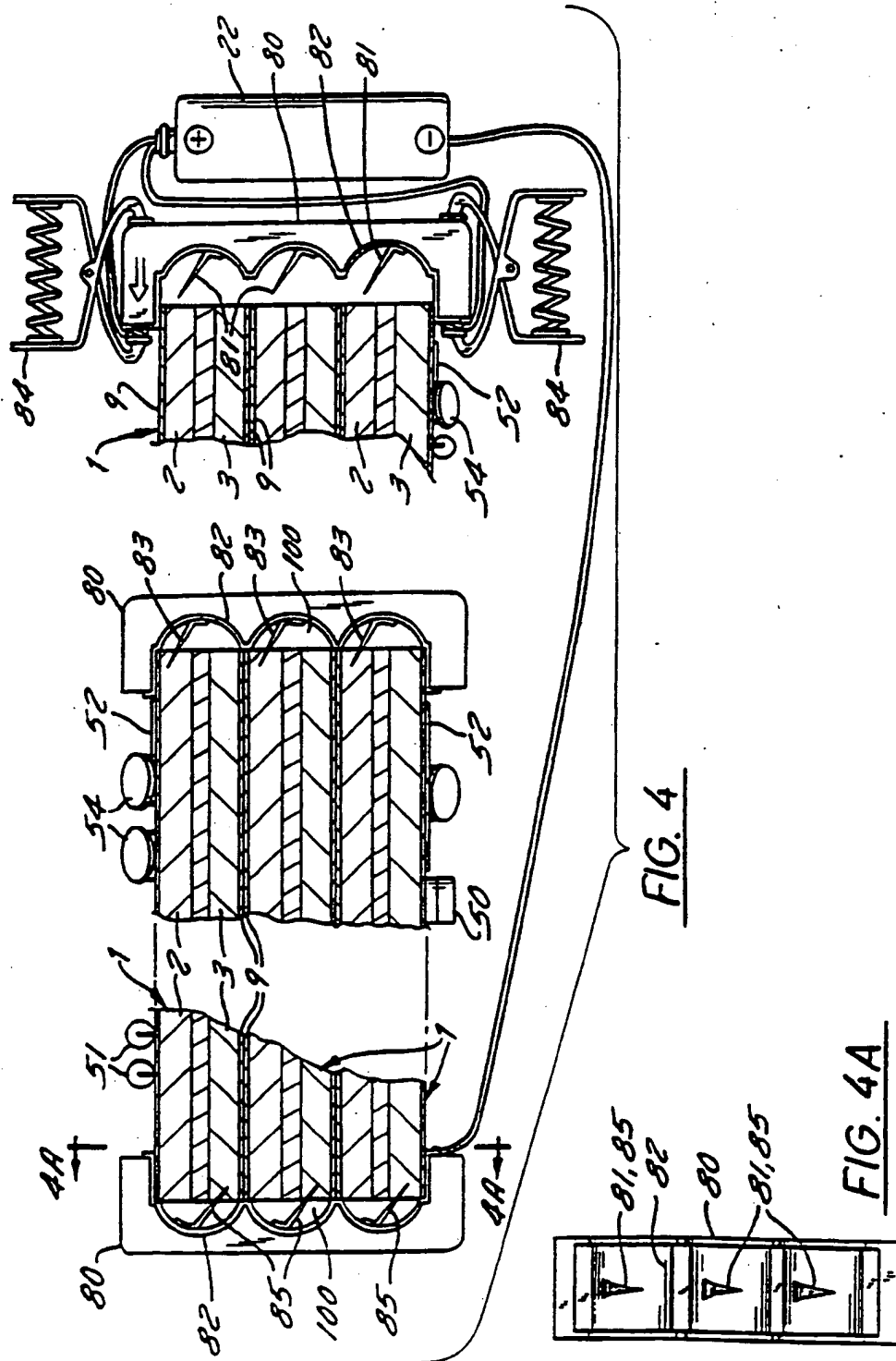
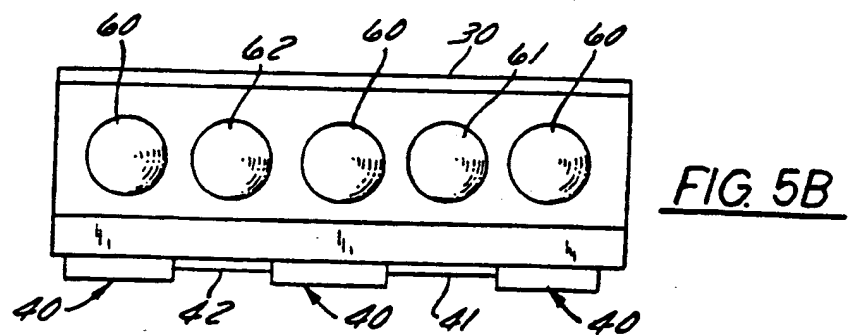
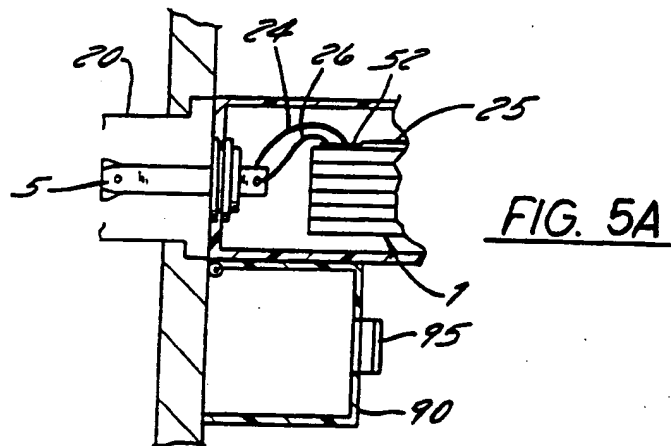
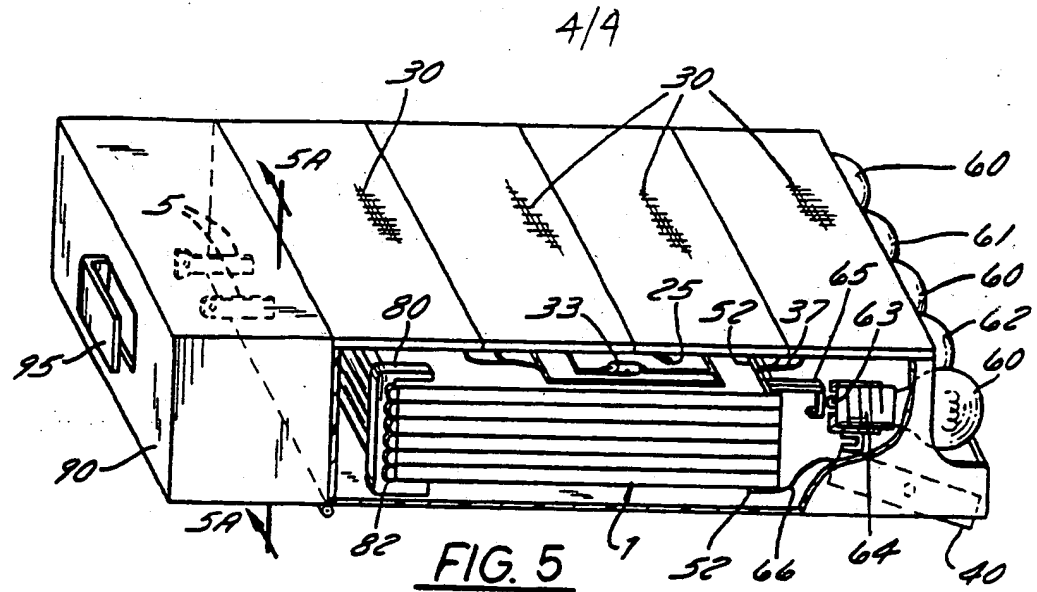


FIG. 3A

3/4





SHEET BATTERIES AS SUBSTRATE FOR ELECTRONIC CIRCUITSBackground of the Invention

The present invention relates to uses of single or laminate layer(s) of sheet battery as the support substrate for the electronic circuitry they power and for the circuitry used to recharge the battery.

Electronic circuits are becoming more compact using surface mounting and flex circuitry techniques. These circuits have for the most part been separate from battery units, power supply and voltage converters.

The size and multiplicity of the sheet battery substrate determines the voltage and current capacities of the power unit. Sheet battery technology has achieved four times the power per unit mass of conventional batteries. Continuous progress in thin sheet battery or electronic cell development has progressed to reliably rechargeable power units emerging from fabrication in continuous web form. This allows the expansion of its use to circuit substrate for long-term use items needing minimal mass with built-in recharging capabilities.

Summary of the Invention

It is an object of the present invention to consolidate electronic circuitry by mounting the circuitry on the neutral insulating covering of sheet battery material on one or both sides of the battery unit.

Printed circuit and surface mounting techniques and evaporated circuit techniques are used on both fiberboard and flex circuit films. Applying these to sheet battery



substrate gives greater performance per unit mass for electronic circuitry.

It is another object of the invention to laminate the sheet battery substrate making tiers of circuitry and increasing the power component allowing spacing for ventilation if needed.

It is yet another object of the invention to insert circuit bearing plugs in the sheet battery substrate which connect wiring between the surface of circuitry whether on two surfaces per sheet or in a laminate structure of battery substrate.

It is yet another aspect of the invention to use an inner surface wired edge clip with pivoting probe leads, the number equal to the number of laminate layers and spaced to center the pivot at the midpoint of each sheet of battery included, which when put in a direct current (DC) circuit are attracted to either the anode or cathode segment of the sheet battery depending on charge. With probes aligned the clip is driven into the sheet battery substrate anchoring the probes in the anode substrate or the cathode substrate of the sheet(s) of battery. These prongs attached to the conductive lining of the clip comprise one lead of the battery unit. A clip applied with the reverse charge comprises the second battery lead.

The above technique allows ease of assembly and in field replacement of the battery unit for circuit structures independent of the battery unit.

It is yet another aspect of the invention to apply recharging circuitry to the battery substrate for, for

example, solar cell chargers or alternating current (AC) wall current trickle chargers.

It is yet another aspect of the invention to build a functional product using the components of this invention, as, for example, a solar, AC trickle chargeable flashlight adding plug prongs and housing, a surface of solar cells, rocker switches and bulbs enabling use at home and in the wilds.

#### Brief Description of the Drawings

10 FIG. 1 is a perspective illustration of a printed circuit on a sheet battery substrate using surface mounted electronic assembly with traversing wiring and power and ground access.

FIG. 1A is a partial cross-sectional view of a sheet 15 battery as seen at line 1A-1A of Figure 1.

FIG. 2 is a perspective illustration of a printed circuit with surface mounted components on both surfaces of the sheet battery substrate and conventional mounted components on inserts accessing the circuit wiring on both 20 surfaces.

FIG. 2A is a perspective illustration of the underside of the raised insert shown in Fig. 2.

FIG. 2B is a partial cross-sectional view of the insert shown at line 2A-2A of Fig. 2.

25 FIG. 3 is a cross-section illustration of multiple layers of laminated sheet battery substrates with surface mounted electronics on the outer surfaces and integrated circuit-type electronics, such as evaporated circuitry, on

the surface of the middle sheet of the laminated sheet batteries, and inserts accessing all layers.

FIG. 3A is a partial sectional view of the evaporated circuitry on the inner surface of laminated sheet batteries  
5 shown at line 3A-3A of Fig. 3.

FIG. 4 shows a means of inserting ground and power battery leads into the sheet batteries during fabrication and in repair procedures.

FIG. 4A is a side view of the clip shown at lines 4A-4A  
10 in Fig. 4.

FIG. 5 is a perspective illustration of an AC and solar cell rechargeable flashlight powered by a sheet battery stack highlighting the general structure, light array and AC adapter.

15 FIG. 5A is a partial cross-section of the AC adapter shown in Fig. 5.

FIG. 5B is a front view of the flashlight shown in Fig. 5.

#### Description of the Preferred Embodiments

20 Referring to FIG. 1, circuit with components 50-51, 53-54 and wiring 52 are formed as an integral part of sheet battery 1, as for example, the lithium vanadium rechargeable sheet battery developed by Henry and Steve Hope of Hope Industries, Inc., and described in U.S. Patent 4,576,883  
25 issued March 18, 1986; U.S. Patent 4,794,059 issued December 27, 1988; and U.S. Patent 4,960,655 issued October 2, 1990. Sheet battery components shown in Fig. 1A are comprised of a power layer 2 and a ground layer 3 with an electrolyte therebetween. The top and bottom outer surfaces 9 of the

sheet battery are covered with a non-conducting substrate. Surface mounting techniques are used to adhere the components to the surface 9 and the leads are fused to the printed circuit by wave soldering and use of adhesives.

- 5 Power 2 and ground 3 from the sheet battery are accessed using inserts from wiring 52.

Referring to FIG. 2, battery sheet 1 has circuitry on the upper and the lower surfaces 9. Inserts 70 connect the upper and lower surfaces of the sheet battery through wiring  
10 52i. One insert also has the role of mounting location for an integrated circuit chip 50. Leads 52i and connector adhesive 57 connect components 56 to wire 52 on the printed circuit on the upper and lower surfaces of the sheet battery. The two renditions of the larger insert 70 show  
15 its upper and lower surfaces. Battery lead connector 80 similar to the connector described in Fig. 4 is used here on a single battery sheet. Battery lead connector 80 houses the power or ground probe 81 shown on edge wiring connections to power 2 and to ground 3, here giving a second  
20 ground lead source.

Referring to FIG. 3, we have a laminate of printed circuit or integrated circuit layers of sheet battery 1. Insert 75 made from plastic or other non-conductive material serves the multiple layer structure by feeding out through  
25 leads 52i at the appropriate layers and in the appropriate directions. Insert 75 can be a single insert or a laminate of single inserts with connector adhesive 57 extending outward making it simple to connect insert wires 52i with circuit wires 52. Insert 75 carries on its outer layer

integrated circuits or other similar chips which have leads 52i feeding components on the various layers. Contact with wiring 52i on the inserts 70 and 75 to wiring 52 on sheet battery surface 9 is by wave solder or conductive adhesives. 5 57 or conductor wired flex circuit tape. Concave hollows 10 keep the insert substrate from the battery power 2 and ground 3 substrates. Air pockets or non-conducting plastic can fill these spaces insuring no grounding out of the sheet battery.

10 Referring to FIG. 4, a means to take advantage of the polarized nature of a common battery 22 in placing the battery connector clips 80, uses leads from the positive end of the common battery 22 to direct connector pins 81 to the power substrate 2 and the negative pole of the common 15 battery 22 to direct connector pins 81 to the ground substrate 3 of sheet battery 1. The auxiliary common battery allows initial setting of leads for a sheet battery stack. These clips handle multiple layers of sheet battery substrate 1. Clips 80 for single sheets of battery sheet 1 20 as shown in FIG. 2 are made and applied using the same principle.

The battery lead clips 80 have a support substrate lined with a conductive sheet 82 in contact with the lead pins 81. Concave hollows 100 for each layer keep the 25 conductive layer from contacting the power 2 or ground 3 of the battery, but allow contact with the insulating surface 9 of the layers. Clamps 84 from the cathode of the auxiliary common battery 22 to the clip 80 cause the pins 81 to favor the power 2 of the sheet battery. Once the polarity holds

the pins 81 in the desired direction, the clip 80 is pushed onto the battery material causing the pins 81 to penetrate the proper layer of the battery substance. Pins 83 and 85 feed into the power 2 and ground 3 of the sheet batteries, 5 respectively.

The choice of circuit construction between the inner and outer layers of the sheet battery defines a preferred rendition of the patent satisfying the inclusion where necessary of mounting individual components on outer surface 10 circuits and reserving the inner surfaces for circuitry that can be rendered using evaporated circuit techniques.

The thickness of the sheet battery intended for use is about 0.02-0.04 inch thick which is not possible to draw with inner definition. The representations here greatly 15 expand the sheet battery's vertical dimension. The hollows 100 are deeper ellipses containing the pin 81. With the design of circuit accommodation for the sheet battery materials in single or multiple layers, the condensation of circuit size is accomplished.

20 Referring to FIG. 5, a flashlight, with circuit board sheet battery 1 supporting a trickle charge circuit 25 drawing power from plug prongs 5 which take AC wall plug 20 power and feeds the power side with lead 26 and ground with lead 24 which feed into wires 52 of a trickle charging 25 circuit on the sheet battery surface. Alternately, it can use solar cells 30 as long as diode 33 prevents current drain when the cells are not in light inputting charge with power line 37 and ground lead (not shown). Light from the sun or a lamp charges the battery through the solar cell

energy absorption. Both charging circuits are common art in the electronics field and solar cells come with recommended circuit schematics.

The flashlight has switches 40 in the on position and  
5 switches 41 and 42 in the off position resulting in bulbs 60 being on and bulbs 61 and 62 being off giving a choice of pattern for bulb illumination as shown in Fig. 5B. The power contact 63 and ground contact 64 from bulb 60 has a permanent power wire 65 and a ground wire 66 going through  
10 switch 40 here in the on position. These wires meet battery circuit wires 52 to complete the circuit powered from the power and ground clips 80.

Flashlight encasement includes the hinged cover 90 over the plug prongs 5 as shown in Figure 5A, which contains a  
15 handle 95 that can be clipped to for belt or lanyard carrying.

This figure shows a practical application of the sheet battery substrate circuit board design. It includes two means of charging the device both having their circuits on  
20 the sheet battery substrate. This simple application gives the basics for using this circuit design for a wide range of applications.

As is shown with the flashlight in FIG. 5, we have eliminated the AC converter by applying a trickle charging  
25 circuit which will allow only one direction of current from the AC power source and will slowly charge the battery from the wall plug. Also, the simple accommodation of the solar cell with a single diode 33 allows the excited solar cell to

charge the battery, but will not allow the drain of power when it is used or stored in the dark.

Layering of printed circuits, with or without ventilation depending on whether circuit components need cooling, using the power substrate for the support substance gives shorter leads. It allows cutting the substrate to the size needed to produce the voltage and current requirements eliminating power supplies and voltage converters. More complicated circuitry can be developed with vertical leads between layers. Also, with integrated circuit components as well as other similarly complicated electronic chips, the outputs to several circuit components is achieved more directly with multilayer configurations as the sheet battery laminate allows.

These developments make significant differences in the portable electronic equipment market from radios and televisions as well as flashlights and medical assists. The smaller and lighter the component the easier it is to use and handle. With the structural nature of the battery material when laminated, these applications can be made of the material as well, as for instance in applicant's co-pending applications entitled "Mosaic Monitor for Higher Resolution and Privacy for Audio Accommodations" filed December 29, 1989, Serial No. 07/459,140, and "Super-CCD With Default Distribution and its Fabrication" filed September 12, 1990, Serial No. 07/581,503. In a CD disk magazine for a television camera, for example, both the control circuit board and the magazine casement are sheet batteries giving sufficient power in the magazine to record



on the contained 100 CD discs. This eliminates the need for a power supply in the camera. Polaroid has achieved this in their film packets having sufficient power in the battery disc to drive the focus and power the flash. The camera  
5 takes more power because laser recording is done on the flexible disk substrates. Were lights required to be powered as well, one could recharge the batteries in the casement and circuit board substrates.

For multiple battery applications as described with the  
10 CD disk magazine, leads from the circuit board substrate battery to the edges of the structural support battery material are provided using the connector clips for both the power and the ground leads allowing recharging from one source to recharge all the battery components in the entire  
15 electronic device.

Application of the circuits to the insulator layer 9 of the sheet battery substrate 1 can be done using several currently employed techniques.

The photo technique of producing printed circuits on  
20 circuits board can be used on the sheet battery substrate. This technique includes coating the sheet battery substrate with copper and over the copper coating is either a photographic or silk screen application to areas where wire circuitry is required. Untreated areas are cleared away in  
25 a corrosive bath which strips copper from the surface. The circuits are completed by surface mounting electronic components on the sheet battery substrate surface. In the case of use of the sheet battery, the copper coating would be done leaving uncoated margins which before the corrosive

bath are dipped or otherwise coated with resistant material as wax preventing corrosion of the sheet battery substrate. Both sides of the sheet battery substrate can be treated this way in one process.

5           Another technique used especially in the inner layers of sheet battery laminates is evaporated circuit techniques where a mask or stencil exposes the areas where wiring is desired and it passes through an area where atomic or spattered conductive substance is applied to construct the  
10 wiring 52. In additional treatments substance comprising the components 58 are evaporated or spatter applied. This technique is used in integrated circuit construction. The inner circuit layer in FIG. 3A shows the appearance of such a circuit greatly enlarged.

15           Inserts have tiered circuitry with wiring meeting the edge of the insert at any level of battery circuitry laminate with wire continuity provided by flex circuit extension so wire surface touches wire surface between the insert and battery laminate layer circuitry. Similarly the  
20 conductive inner surface of the edge clip clamps on circuit wiring.

## I CLAIM:

1. An electronic device comprising a sheet battery having a power layer and a ground layer with their outer surfaces covered by an insulating layer to serve as an electronic circuit substrate, and electronic circuitry placed directly on an outer surface of the sheet battery.

2. The device according to claim 1, further comprising electronic circuitry placed on the other outer surface of the sheet battery.

3. The device according to claim 1, further comprising electronic components fused to the outer surface of the sheet battery.

4. The device according to claim 3, where the electronic components fused to the surface comprises wave soldered components.

5. The device according to claim 3, where the electronic components fused to the surface comprises adhesively applied components.

6. The device according to claim 2, further comprising a hole formed in the sheet battery;  
an insert having electronic circuit wiring thereon placed in the hole; and

the wiring on the insert matches the electronic circuitry on each surface of the sheet battery and provides contact therebetween.

7. An electronic device comprising two or more laminated sheet batteries, each sheet battery having a power layer and a ground layer with their outer surfaces covered by an insulating layer to serve as an electronic circuit

substrate, and electronic circuitry placed on at least one outer surface of the laminated sheet batteries.

8. The device according to claim 7, further comprising electronic circuitry placed on the two outer  
5 surfaces of the laminated sheet batteries.

9. The device according to claim 7, further comprising electronic circuitry placed on a surface of a first sheet battery which is adjacent to a surface of a second laminated sheet battery having no electronic  
10 circuitry thereon.

10. The device according to claim 9, further comprising a hole formed in the laminated sheet batteries; an insert having electronic circuit wiring thereon placed in the hole; and

15 the wiring on the insert matches the electronic circuitry on each surface of the sheet batteries and provides contact therebetween.

11. The device according to claim 10, further comprising electrical contact between the circuit on one  
20 surface with the circuit on at least one other surface by provided vertical wiring placed in the insert.

12. The device according to claim 7, further comprising clips for connecting the laminate sheet batteries.

25 13. The device according to claim 12, where the clips further comprise probes, and where the probes on one clip are placed in the power layers, and the probes on another clip are placed in the ground layers, of the laminated sheet batteries, respectively.

14. The device according to claim 13, where each clip has been polarized by an external direct current power source such as a battery to attract the probes to either the power layer or the ground layer in the sheet battery,

5 respectively; and

the probes have been inserted into the power layer or ground layer of the sheet battery by pushing the clip into place thus setting the probes into either the power or ground material of the sheet battery, respectively.

10 15. The device according to claim 1, further comprising a charging circuit for charging the sheet battery with A-C or solar power, and a diode for preventing battery discharge when conditions for charging the battery are not present.

15 16. The device according to claim 1, where the sheet battery has been cut from a web of sheet battery material.

17. A method of constructing an electronic circuit comprising providing a sheet battery having a power layer and a ground layer with their outer surfaces covered by an  
20 insulating layer to serve as an electronic circuit substrate, and placing electronic circuitry directly on an outer surface of the sheet battery.

18. The method according to claim 17, further comprising placing electronic circuitry on the other outer  
25 surface of the sheet battery.

19. The method according to claim 17, further comprising fusing electronic components to the outer surface of the sheet battery.

20. The method according to claim 19, where the fusing of electronic components to the surface comprises wave soldering.

21. The method according to claim 19, where the fusing  
5 of electronic components to the surface comprises applying adhesives.

22. The method according to claim 18, further comprising forming a hole in the sheet battery;  
placing an insert having electronic circuit wiring  
10 thereon in the hole;  
matching the wiring on the insert with the electronic circuitry on each surface of the sheet battery and providing contact therebetween.

23. A method of constructing an electronic circuit  
15 comprising laminating two or more sheet batteries, each sheet battery having a power layer and a ground layer with their outer surfaces covered by an insulating layer to serve as an electronic circuit substrate, and placing electronic circuitry on at least one outer surface of the laminated  
20 sheet batteries.

24. The method according to claim 23, further comprising placing electronic circuitry on the two outer surfaces of the laminated sheet batteries.

25. The method according to claim 23, further  
25 comprising placing electronic circuitry on a surface of a first sheet battery which is adjacent to a surface of a second laminated sheet battery having no electronic circuitry thereon.

26. The method according to claim 25, further comprising, forming a hole in the laminated sheet batteries; placing an insert having electronic circuit wiring thereon in the hole;

5 matching the wiring on the insert with the electronic circuitry on each surface of the sheet batteries and providing contact therebetween.

27. The method according to claim 26, further comprising providing electrical contact between the circuit  
10 on one surface with the circuit on at least one other surface by placing vertical wiring in the insert.

28. The method according to claim 23, further comprising connecting the laminate battery sheets with clips.

15 29. The method according to claim 28, further comprising providing the clips with probes and placing the probes on one clip in the power layers, and the probes on another clip in the ground layers, of the laminated sheet batteries, respectively.

20 30. The method according to claim 29, further comprising polarizing each clip using an external direct current power source such as a battery thereby attracting the probes to either the power layer or the ground layer in the sheet battery, respectively;

25 inserting the probes into the power layer or ground layer of the sheet battery by pushing the clip into place thus setting the probes into either the power or ground material of the sheet battery, respectively.

31. The method according to claim 17, further comprising providing a charging circuit for charging the sheet battery with A-C or solar power, and providing a diode for preventing battery discharge when conditions for
- 5 charging the battery are not present.

32. The method according to claim 17, further comprising cutting the sheet battery from a web of sheet battery material.